

Serial No. 10/672,832

IN THE CLAIMS:

Claim 1 (canceled).

Claim 2 (currently amended): **[[The]] An amplifier circuit comprising a first amplifier; a second amplifier; and a current source, with each of the first and second amplifiers having a gain stage and an output stage as claimed in claim 1, wherein**

the output stage of the first amplifier is coupled to the current source, and when the second amplifier is cascaded onto the first amplifier, the cascading causes the current source of the second amplifier to be connected in series to the output stage of the first amplifier to share the current source; and

the current source **[[(30)]]** is formed by a current mirror using two PMOS transistors.

Claim 3 (currently amended): **[[The]] An amplifier circuit comprising a first amplifier; a second amplifier; and a current source, with each of the first and second amplifiers having a gain stage and an output stage as claimed in claim 1, wherein**

the output stage of the first amplifier is coupled to the current source, and when the second amplifier is cascaded onto the first amplifier, the cascading causes the current source of the second amplifier to be connected in series to the output stage of the first amplifier to share the current source; and

the current source **[[(30)]]** is formed by a current mirror using two NMOS transistors.

Claims 4-6 (canceled).

Claim 7 (currently amended): The amplifier circuit as claimed in claim 2, wherein the first amplifier **[[(10)]]** is a Class A amplifier.

Claim 8 (currently amended): The amplifier circuit as claimed in claim 2, wherein the first amplifier **[[(10)]]** is a Class B amplifier.

Claim 9 (currently amended): The amplifier circuit as claimed in claim 2, wherein the first amplifier **[[(10)]]** is a Class AB amplifier.

Claim 10 (currently amended): The amplifier circuit as claimed in claim 3, wherein the first amplifier **[[(10)]]** is a Class A amplifier.

Claim 11 (currently amended): The amplifier circuit as claimed in claim 3, wherein the first amplifier **[[(10)]]** is a Class B amplifier.

Claim 12 (currently amended): The amplifier circuit as claimed in claim 3, wherein the first amplifier **[[(10)]]** is a Class AB amplifier.

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Claims 13-15 (canceled).

Claim 16 (currently amended): The amplifier circuit as claimed in claim 2, wherein the second amplifier [(20)] is a Class A amplifier.

Claim 17 (currently amended): The amplifier circuit as claimed in claim 2, wherein the second amplifier [(20)] is a Class B amplifier.

Claim 18 (currently amended): The amplifier circuit as claimed in claim 2, wherein the second amplifier [(20)] is a Class AB amplifier.

Claim 19 (currently amended): The amplifier circuit as claimed in claim 3, wherein the second amplifier [(20)] is a Class A amplifier.

Claim 20 (currently amended): The amplifier circuit as claimed in claim 3, wherein the second amplifier [(20)] is a Class B amplifier.

Claim 21 (new): The amplifier circuit as claimed in claim 3, wherein the second Amplifier is a Class AB amplifier.